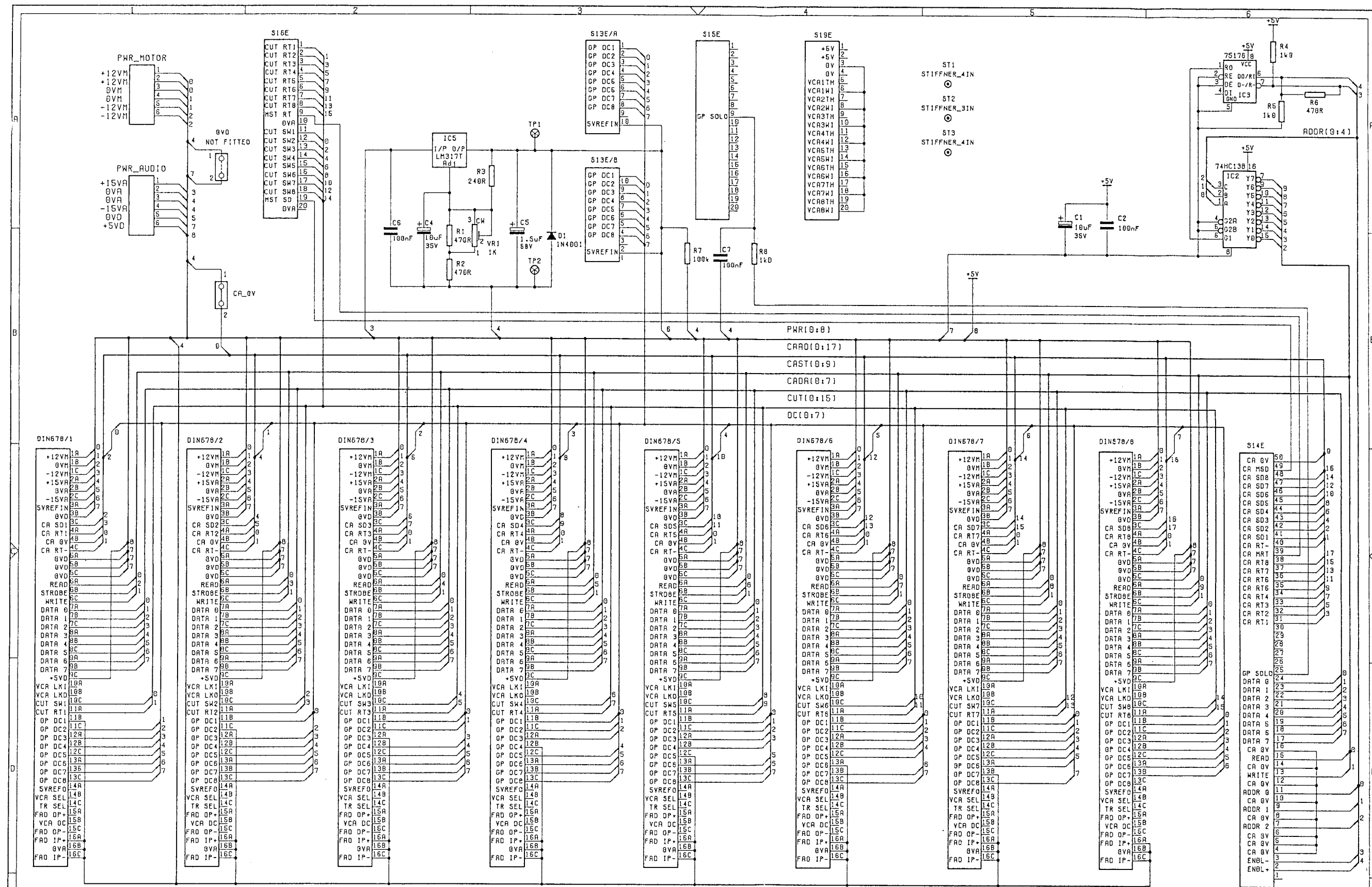




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REV	ISS	DATE	DETAILS	
0	A	7 MAR 91	NEW DRAWING	BC
1	A	21 MAR 91	ECO 4K/723 1K RESISTOR AND 100NF CAPACITOR ADDED	BC
2	A	8 MAY 92	ECO 4K/954 R4 R5 WERE 10K NOW 1K	MM
3	A	19 JUN 92	ECO 4K/978 INTRODUCE ISSUE GH PCB	MM
4	A	19 AUG 92	ECO 4K/992 VR1 80M CORRECTION ONLY	MM
5	A	19 AUG 92	ECO 4K/1005 R4.5 80M CORRECTION ONLY	MM
6	A	12 NOV 92	ECO 4K/1017 NOW ACHIEVED BY ECO 4K/1056	MM
7	A	12 NOV 92	ECO 4K/1056 R6 WAS 100R NOW 470R	MM

PCB ISSUE GH

USED ON SL678 A2

TITLE ULTIMATION
GROUP BUSCARD

DRG.NO. T82358.71

SHEET 1 OF 1

Solid State Logic